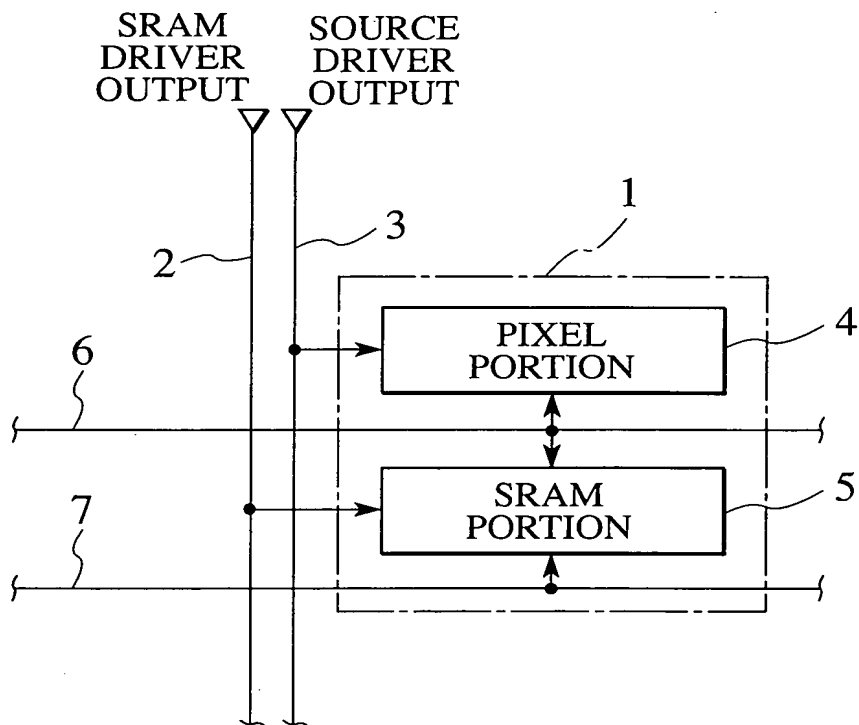


100343



SOURCE DRIVER OUTPUT      SRAM DRIVER OUTPUT

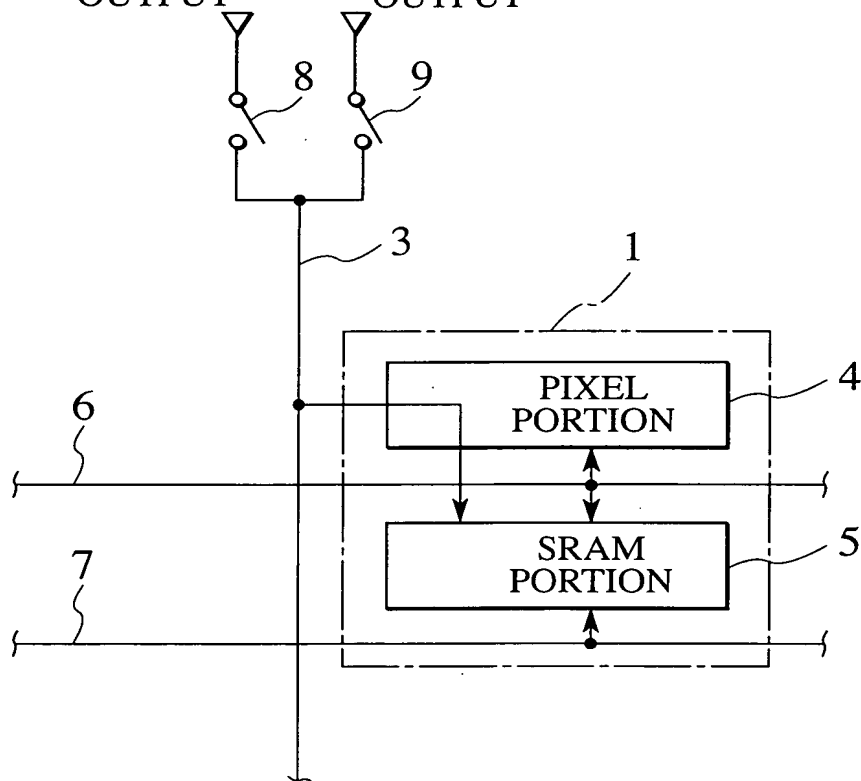


Diagram illustrating a pixel circuit (10) structure. The circuit includes a SOURCE DRIVER (19) connected to a vertical line (11). The vertical line (11) is connected to a horizontal line (17) and a horizontal line (18). The horizontal line (17) is connected to a PIXEL PORTION (100). The horizontal line (18) is connected to an SRAM PORTION (200). The PIXEL PORTION (100) and the SRAM PORTION (200) are connected to each other via a vertical line (11). The PIXEL PORTION (100) and the SRAM PORTION (200) are connected to each other via a vertical line (11).

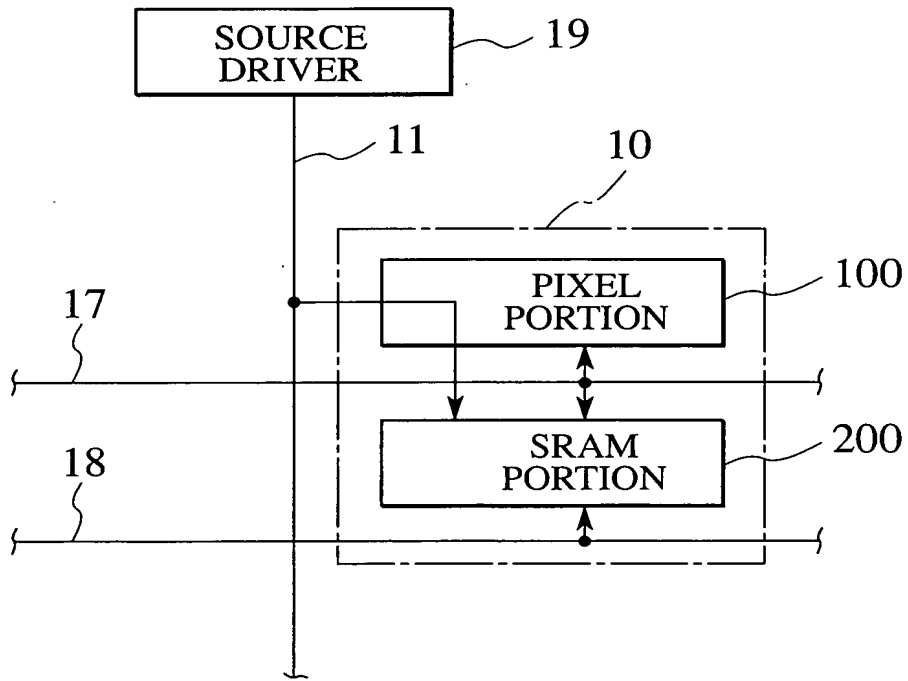


FIG. 4

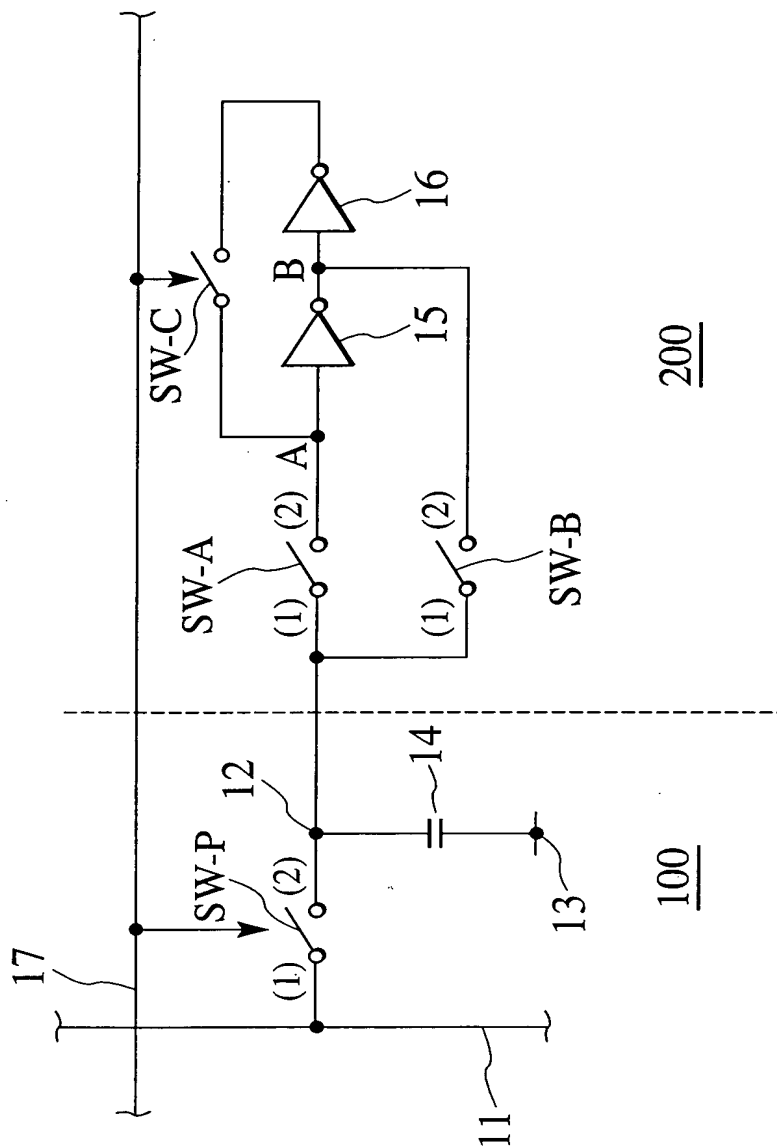


FIG. 5

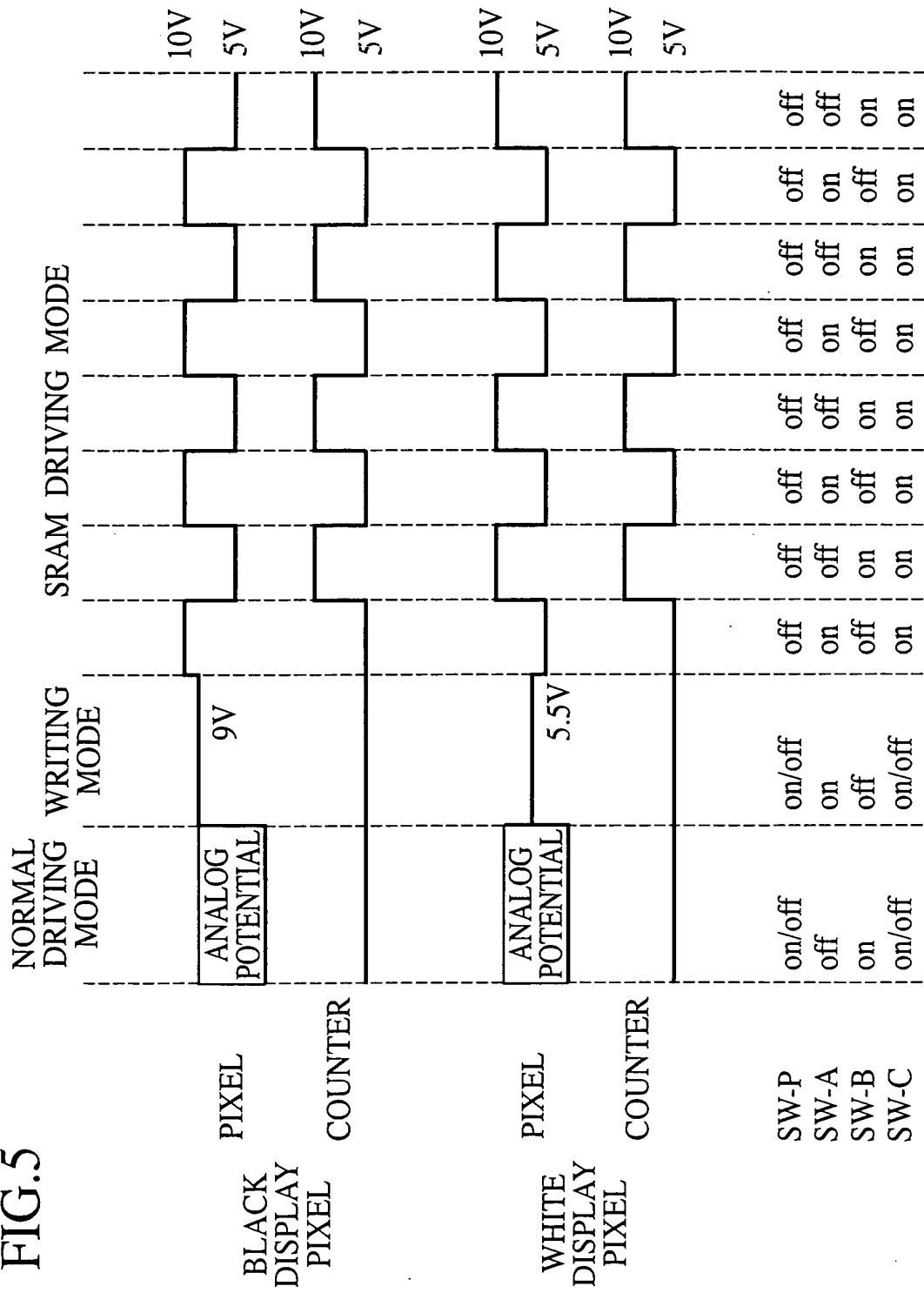


FIG. 6

